

FIG. 1

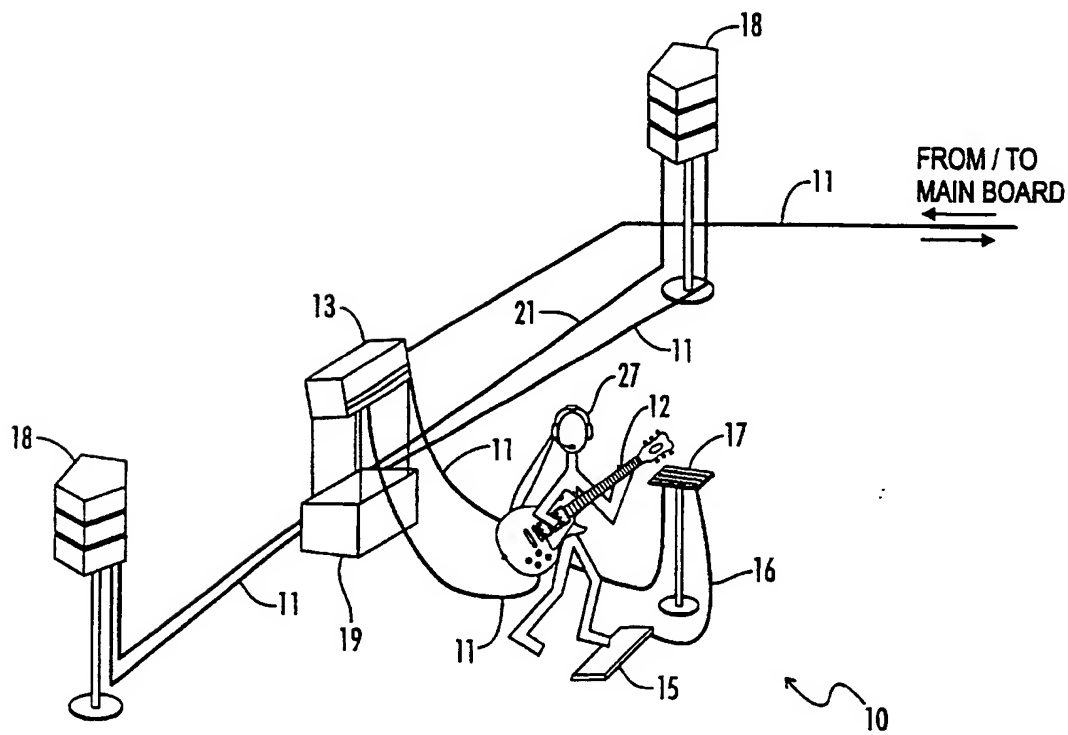


FIG. 2

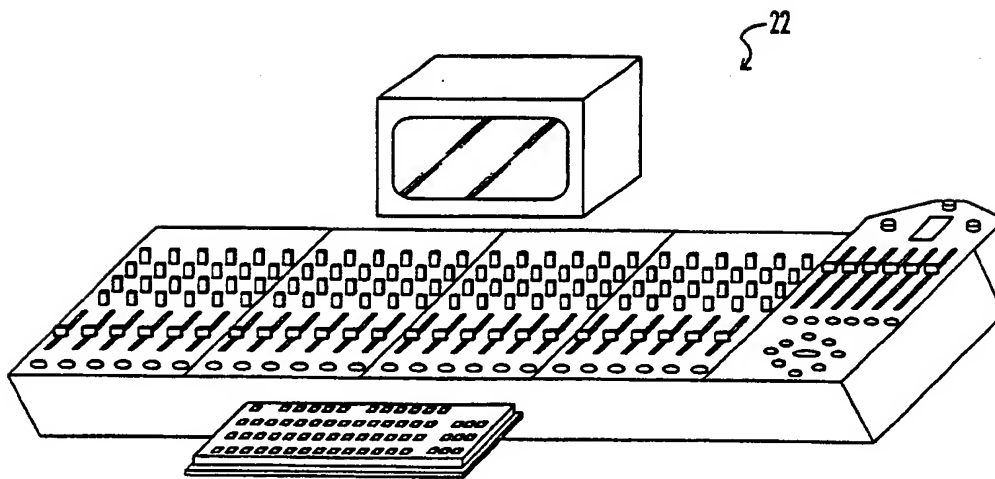


FIG. 3

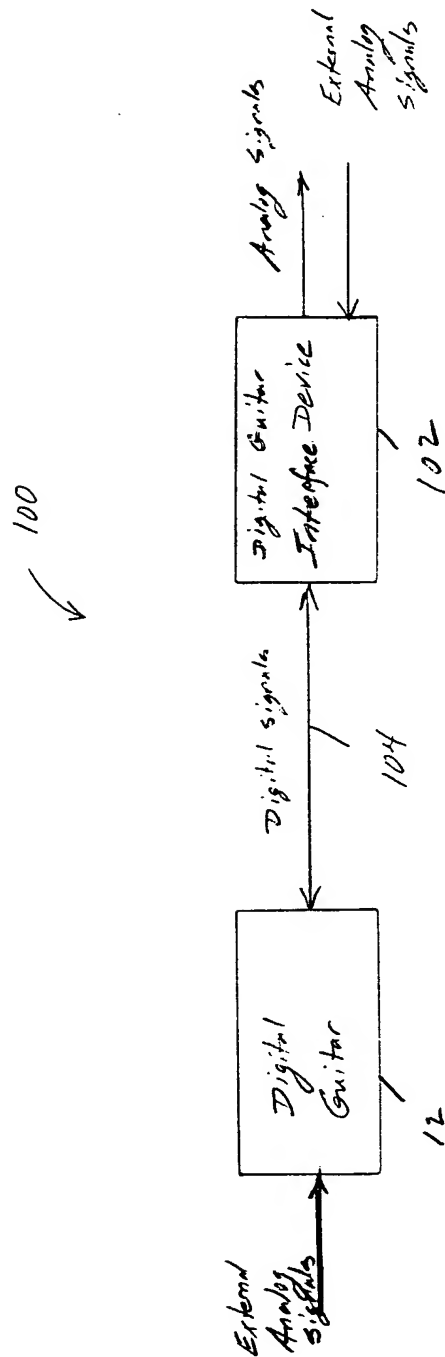


Fig. 4

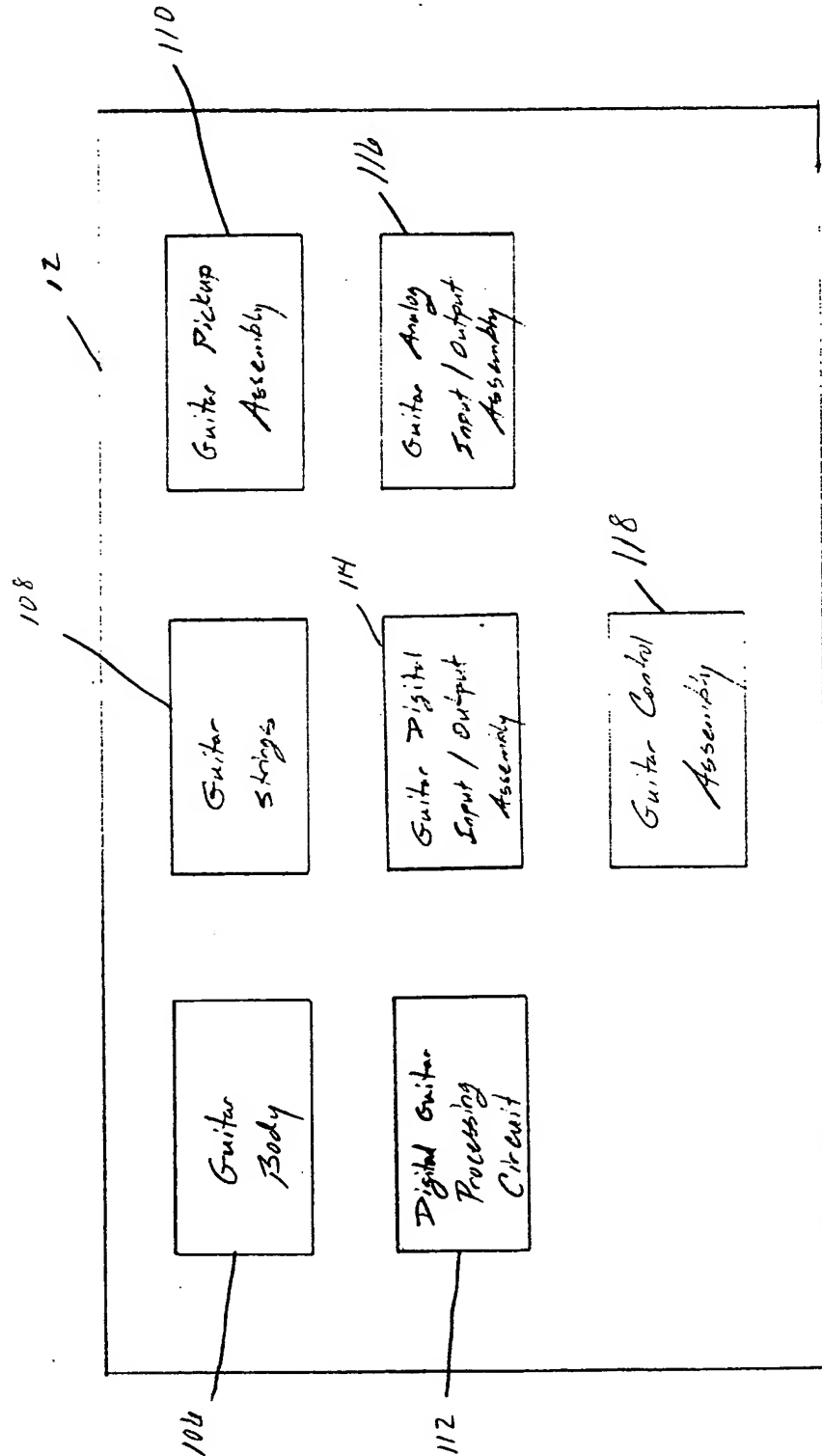


Fig. 5

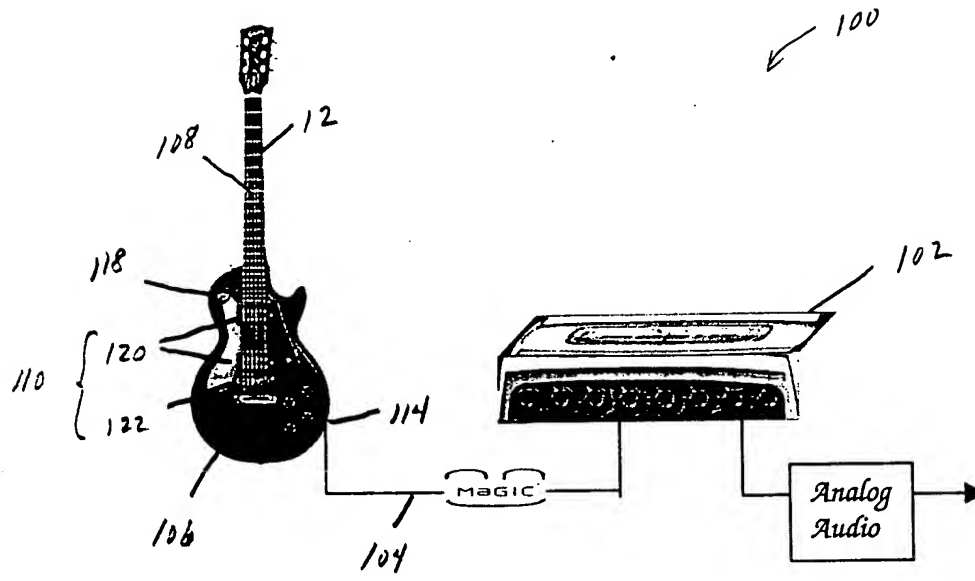


Figure 6

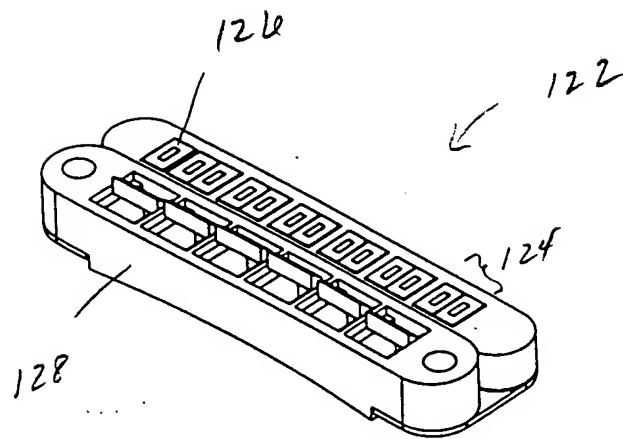


Fig. 7

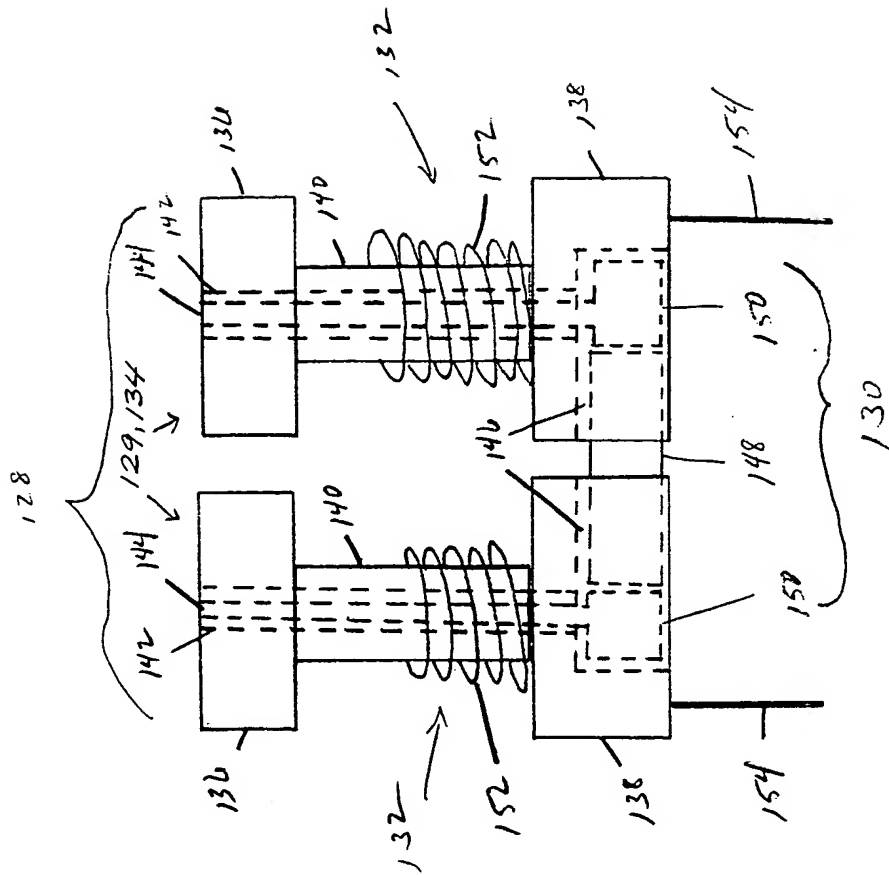


Fig. 8

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Digital Guitar Processing Circuit

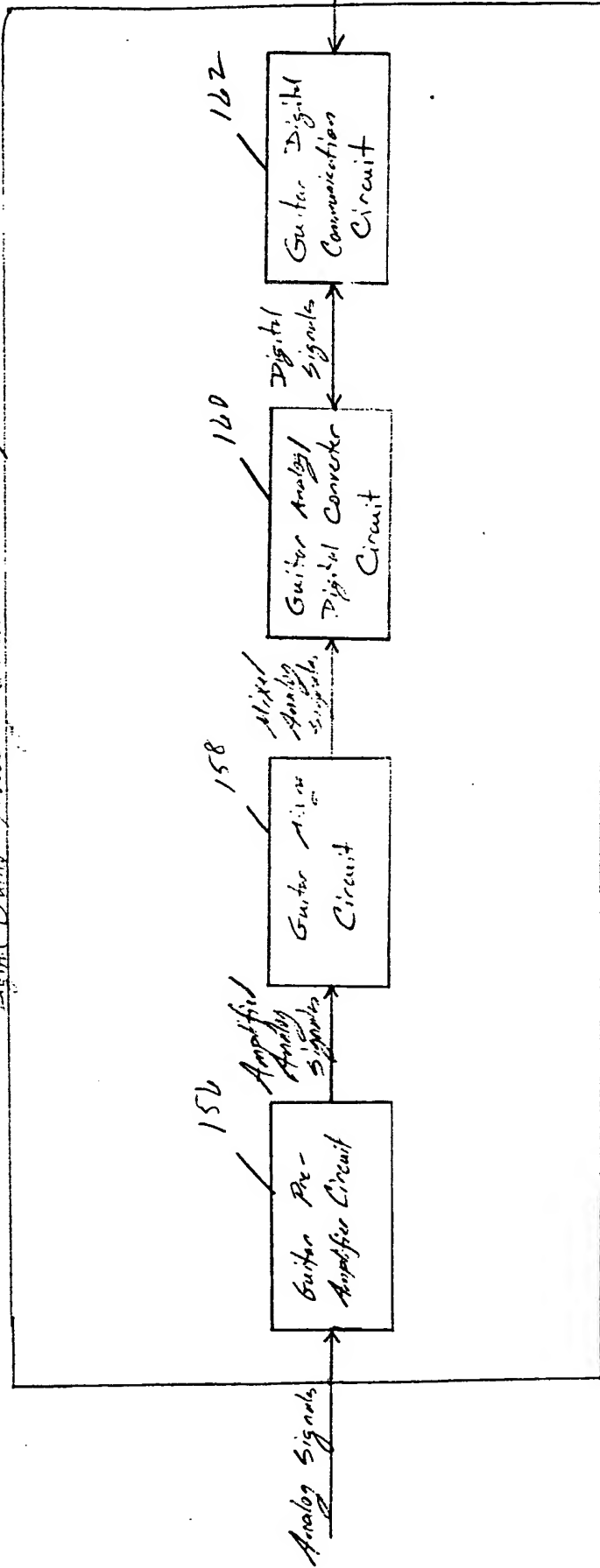


Fig. 9

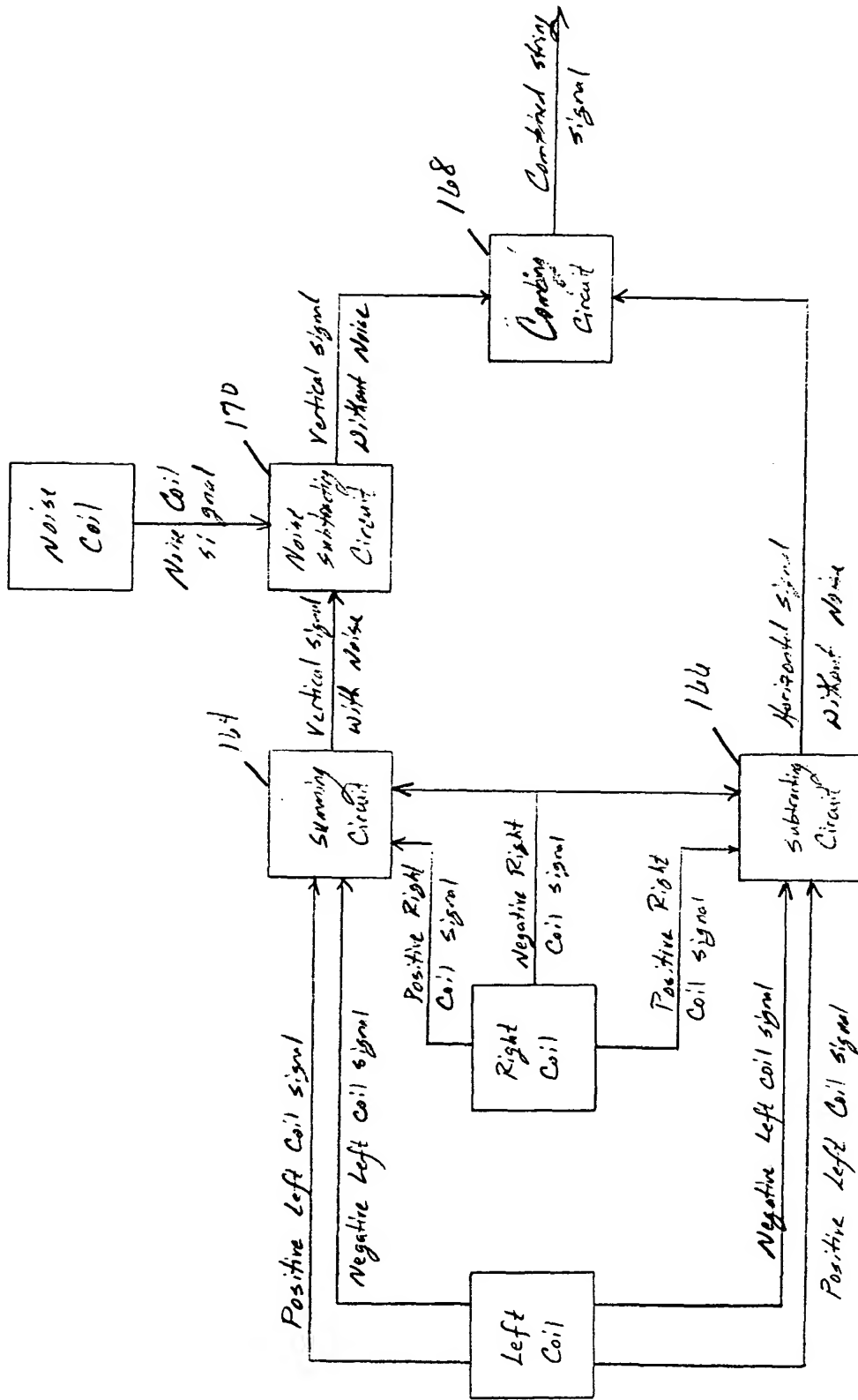


Fig. 10

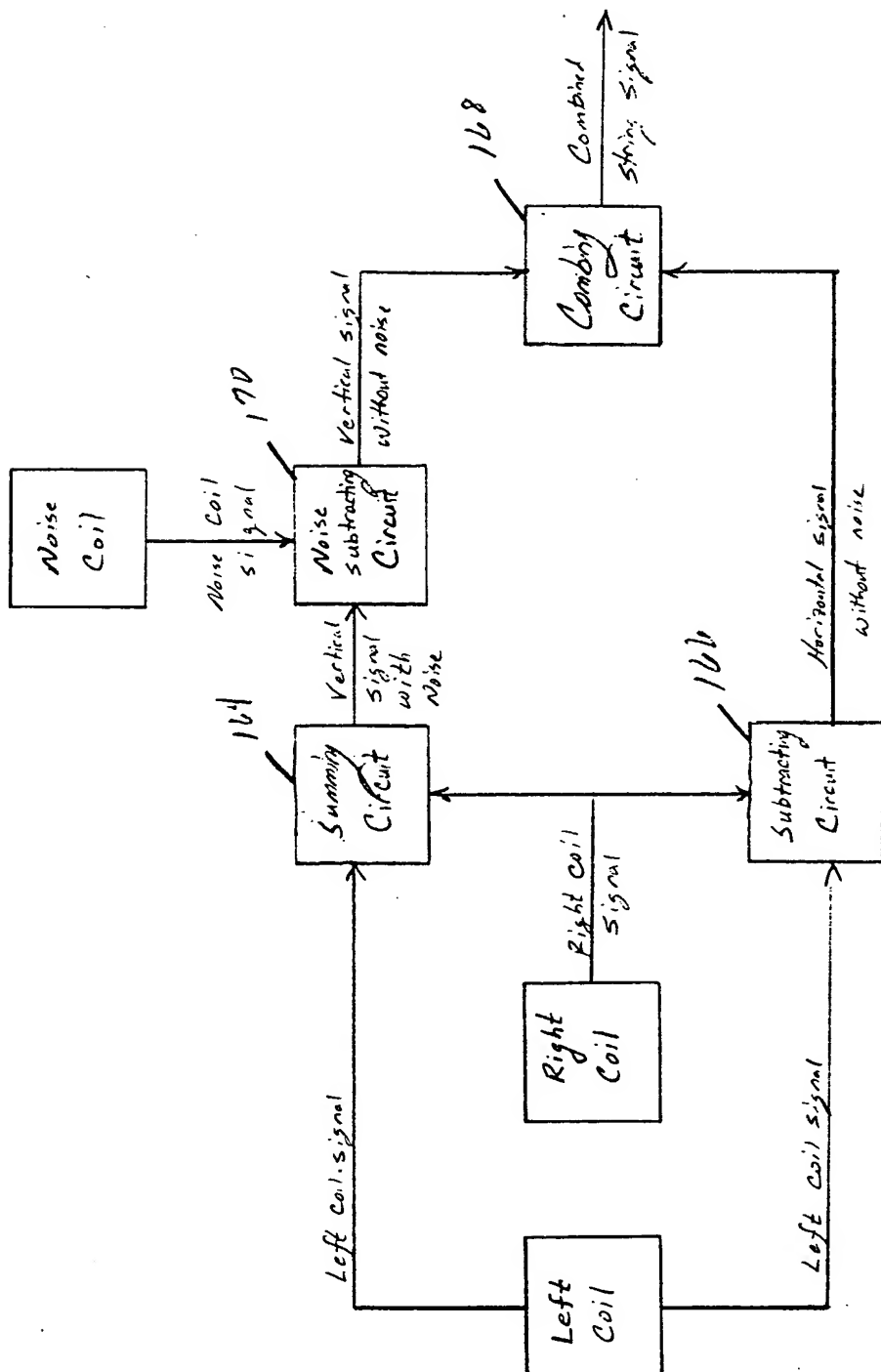


Fig. 11

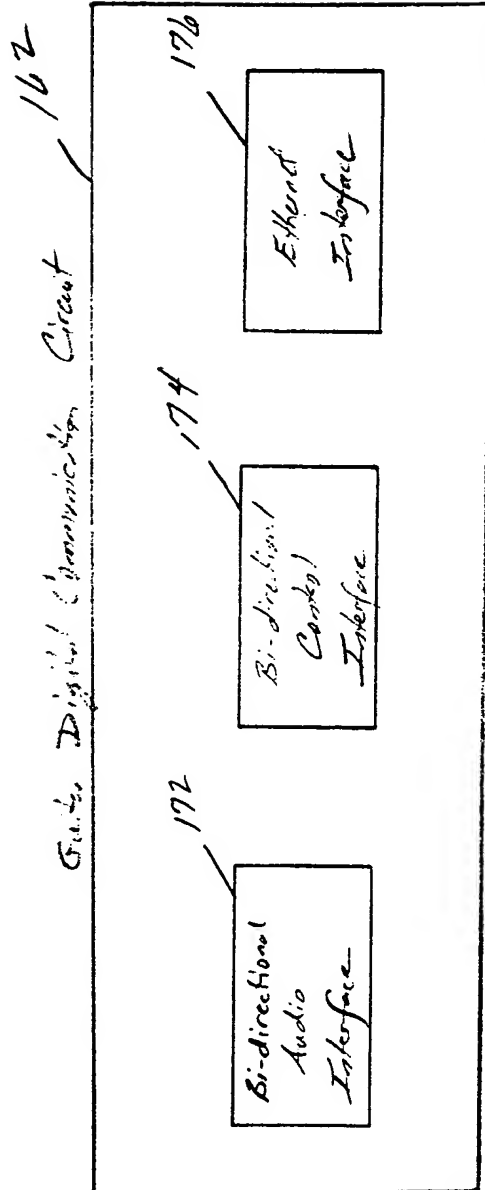


Fig 12

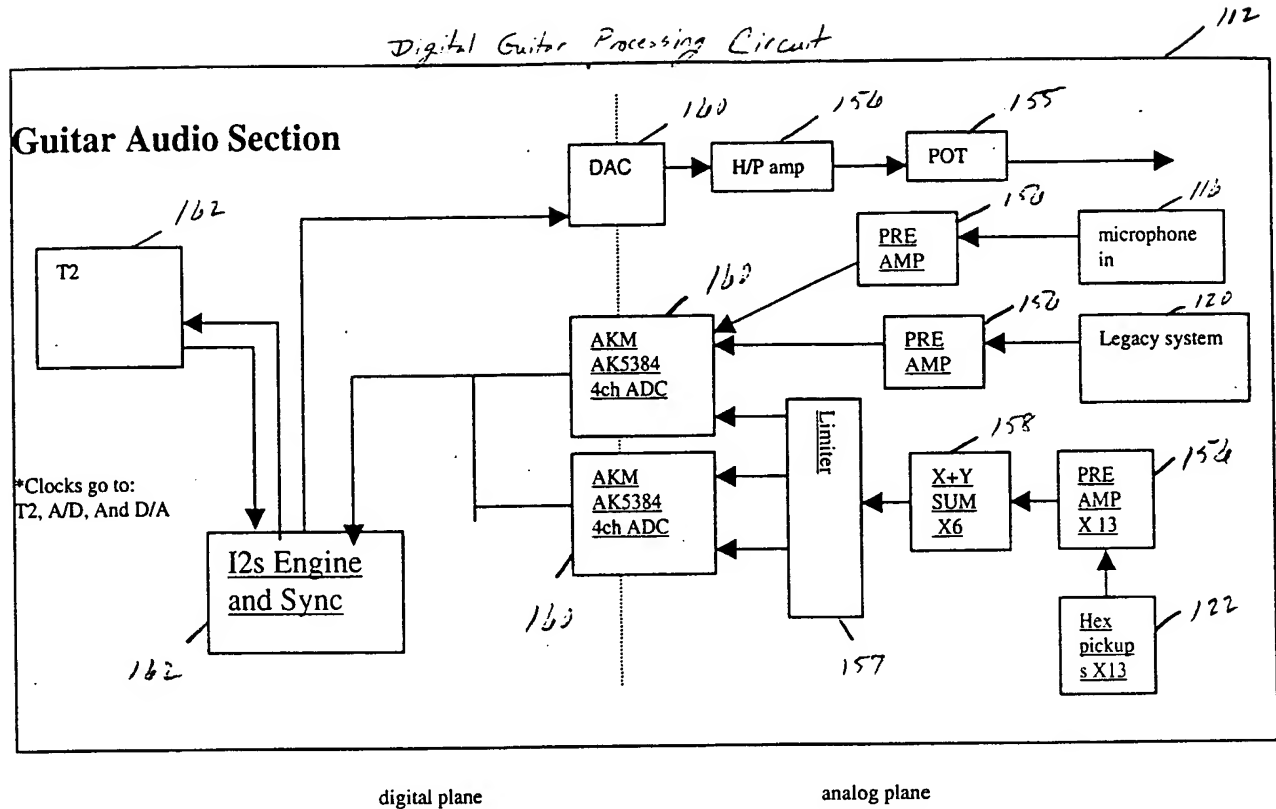


Figure 13

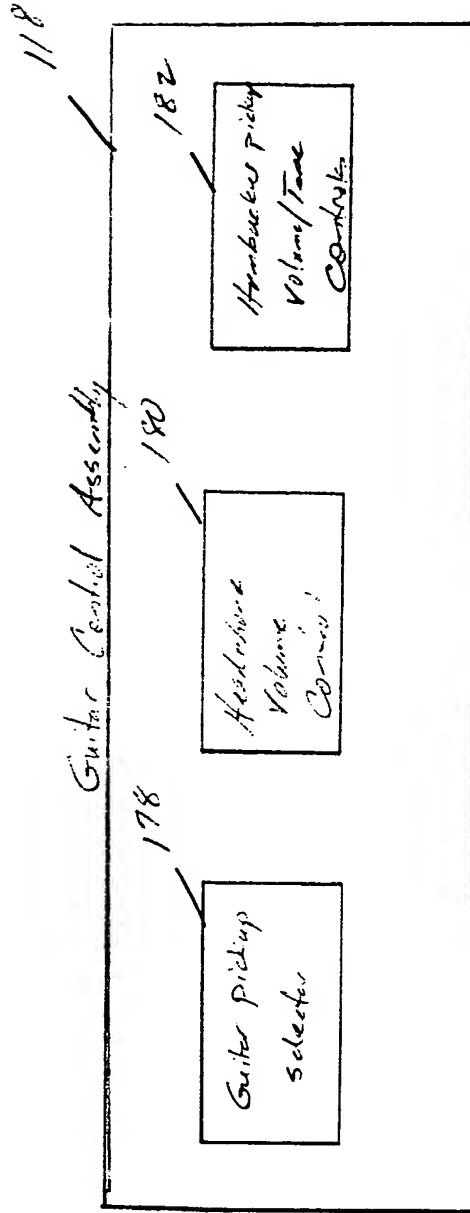


Fig. 14

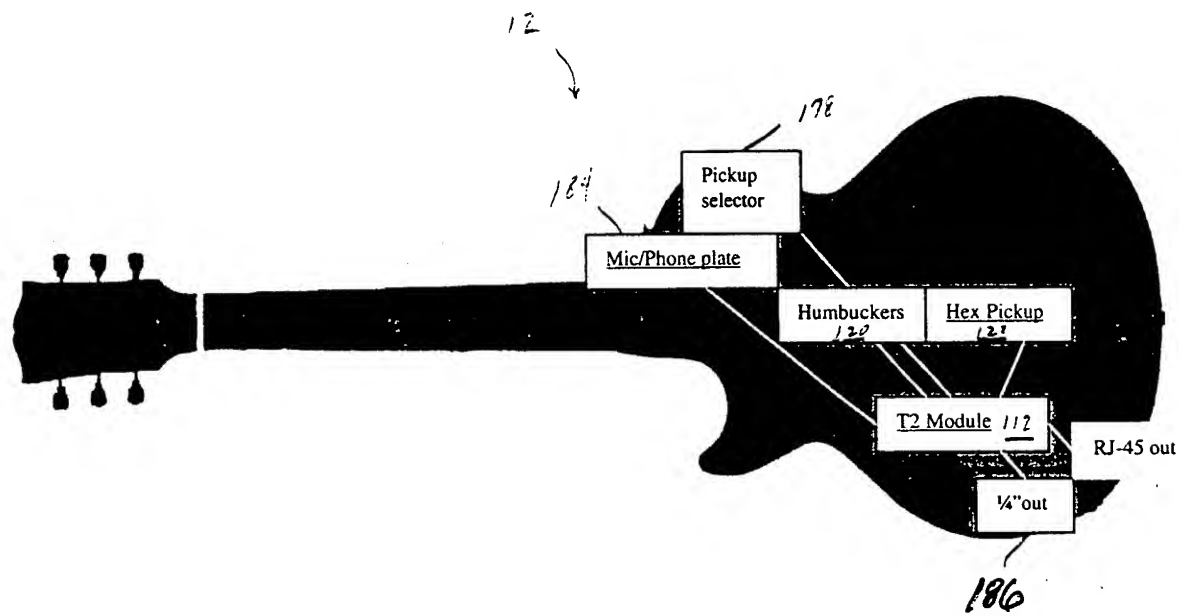
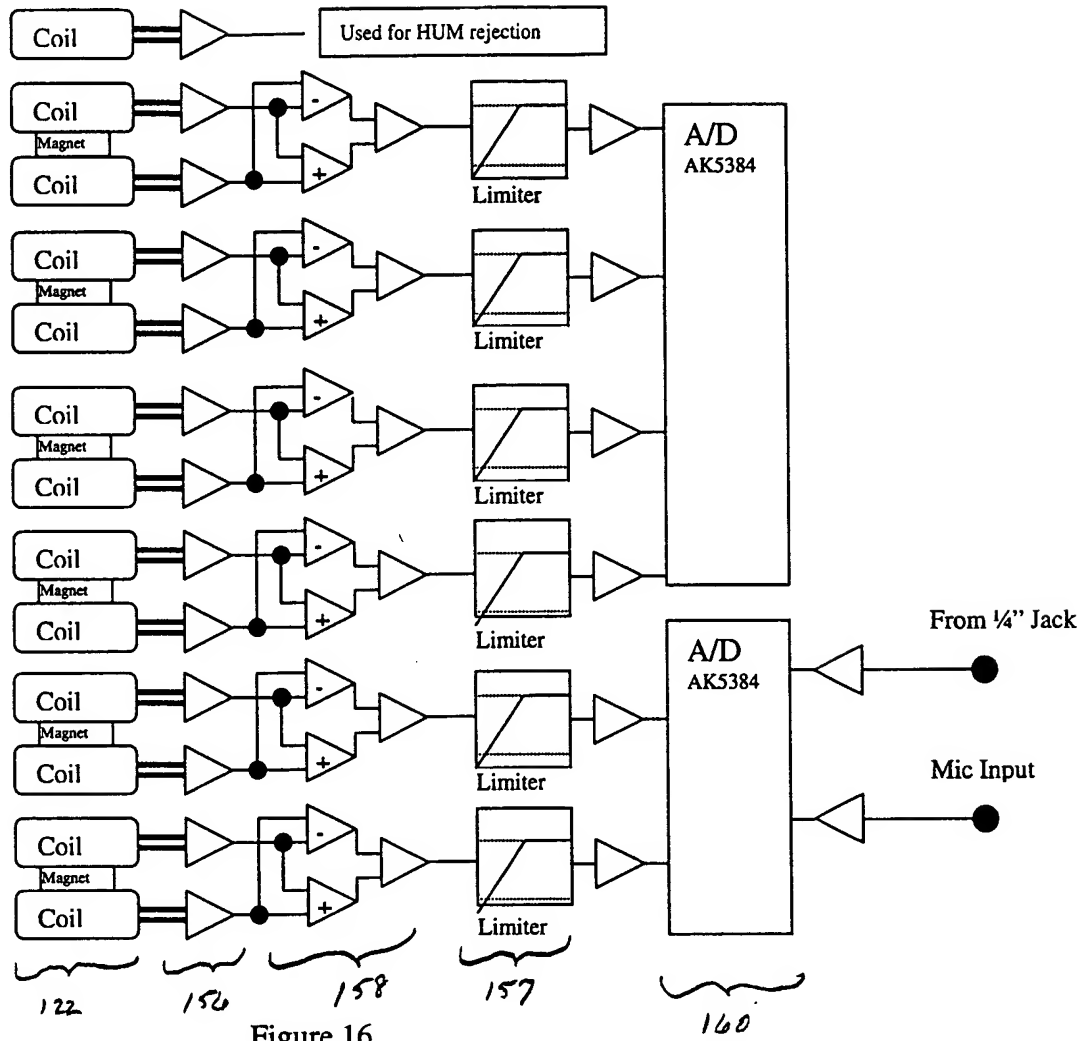


Figure 15

Preamp Section



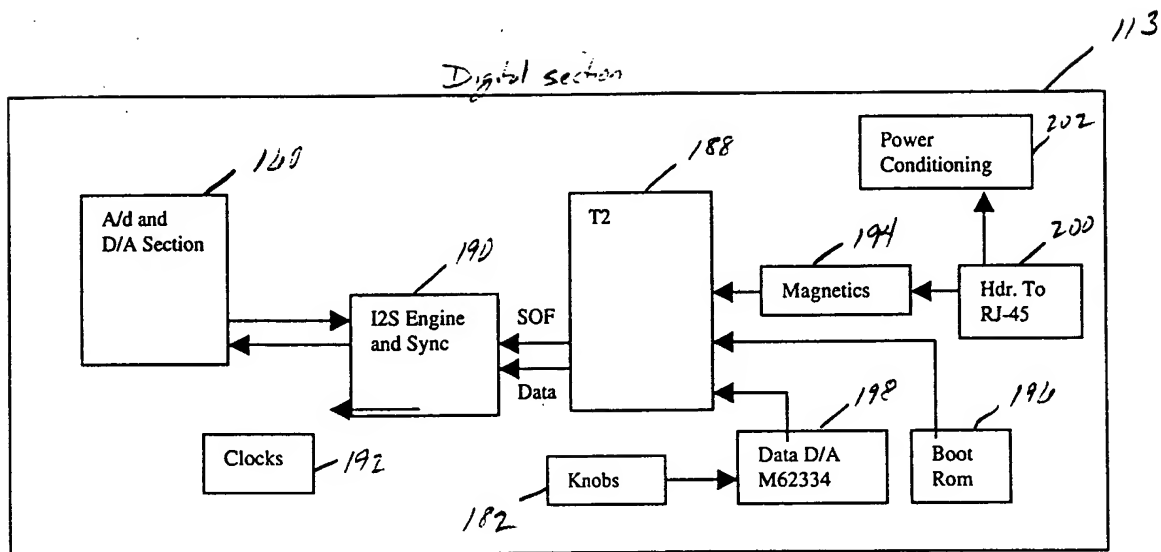


Figure 17

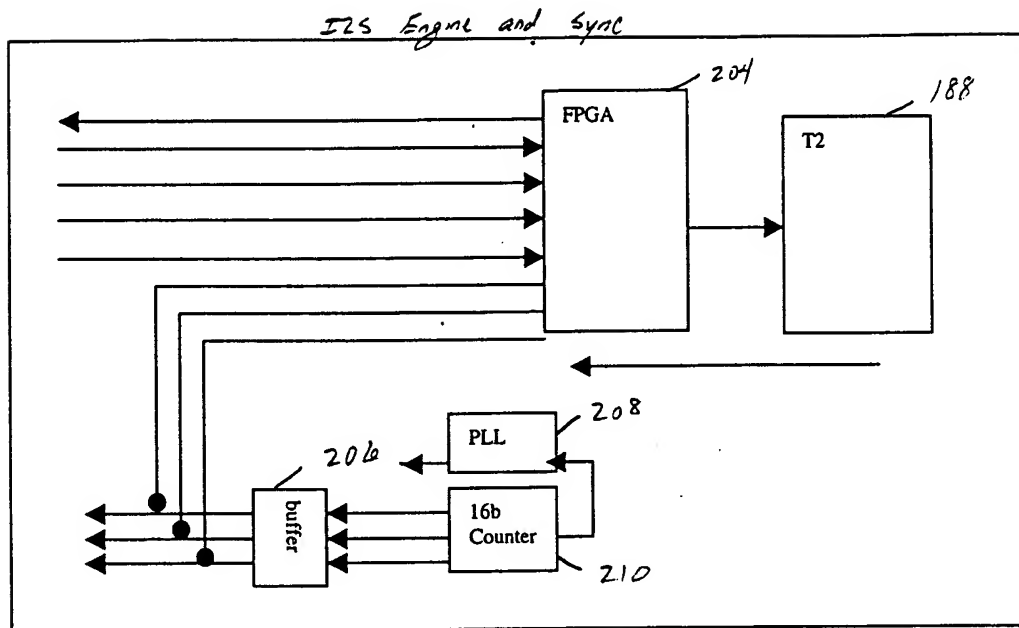


Figure 18

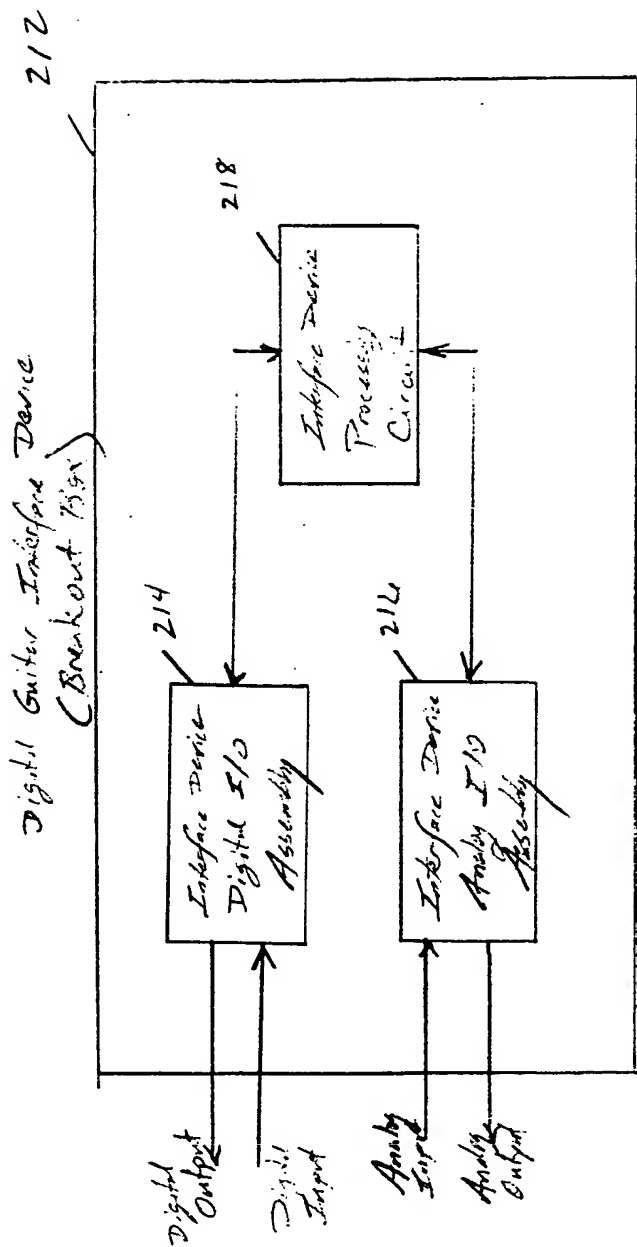


Fig. 19

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Interface Device Processing Circuit

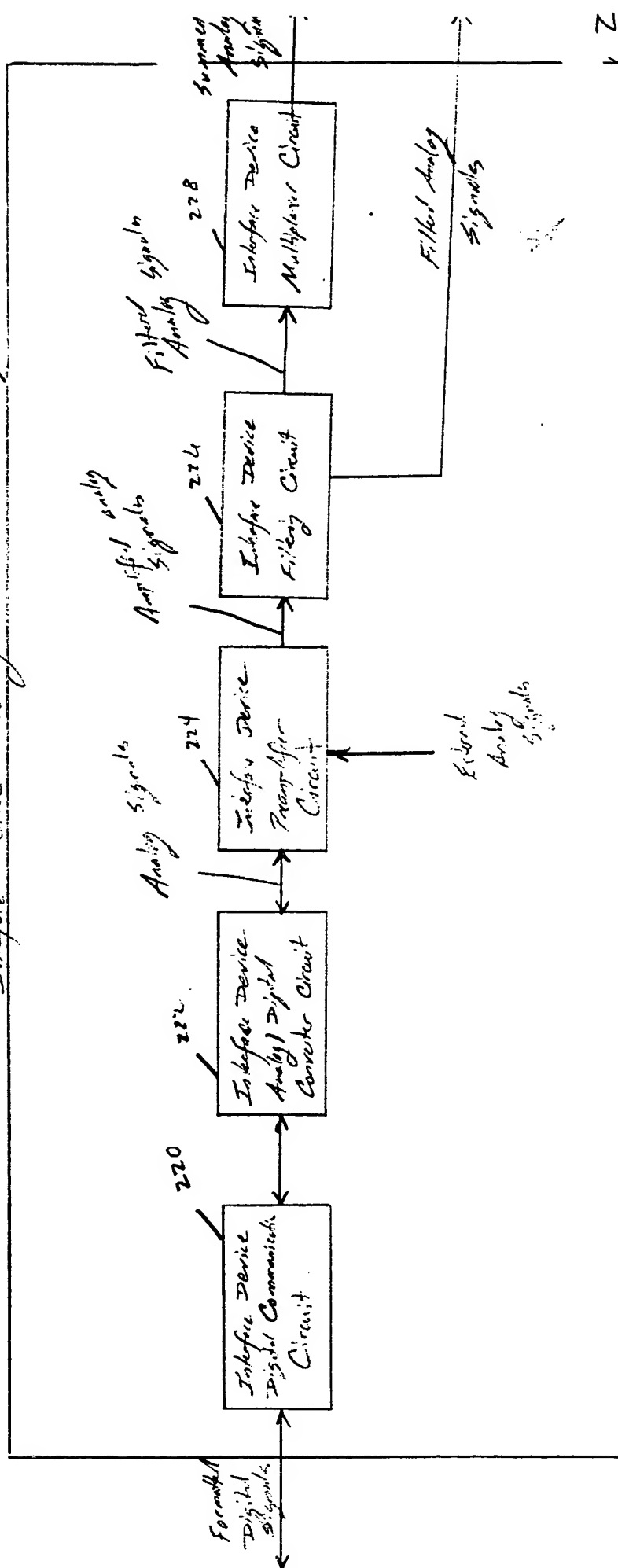


Fig. 20

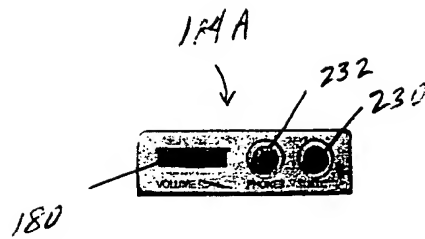


FIG. 21A

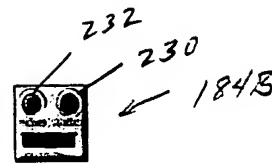


FIG. 21B

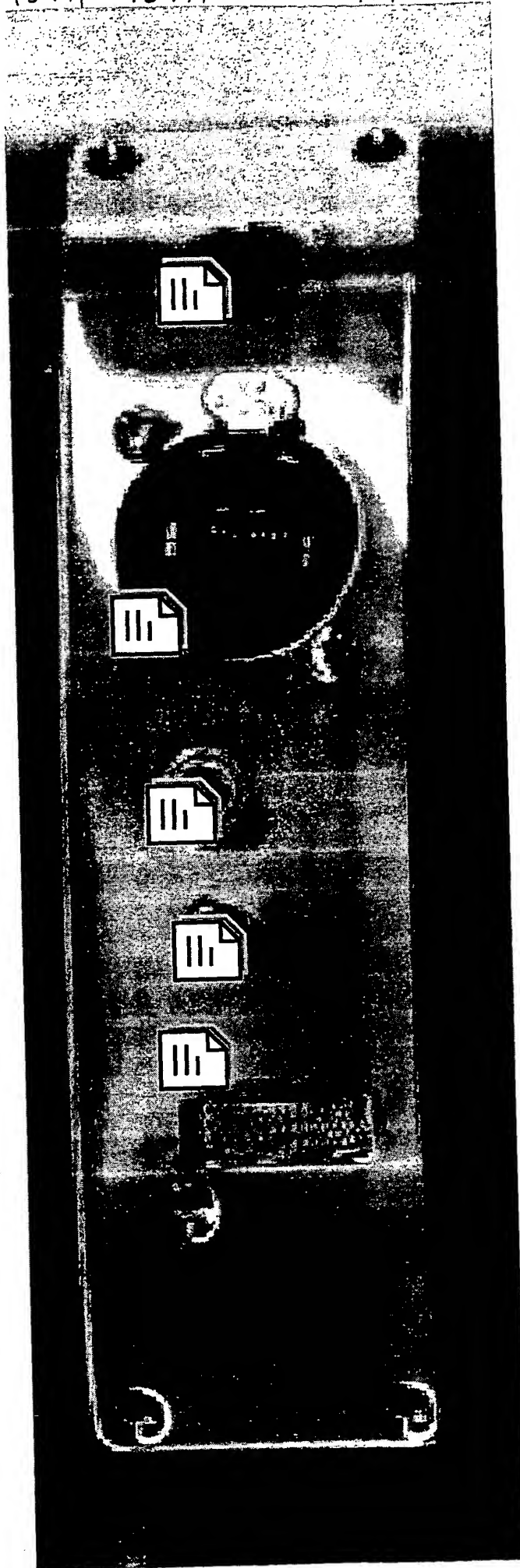
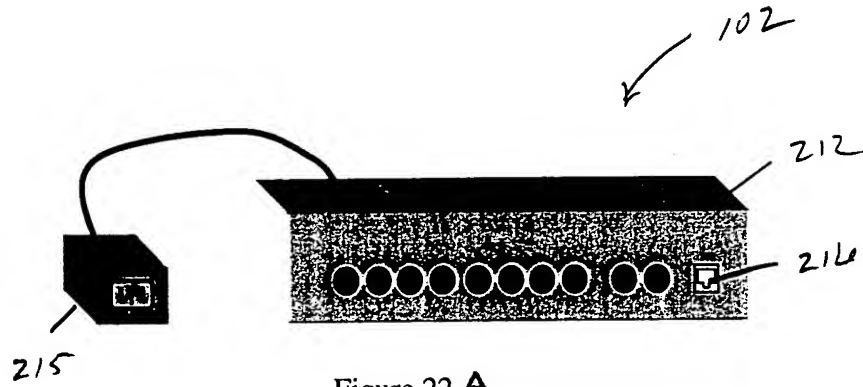


FIG. 21C



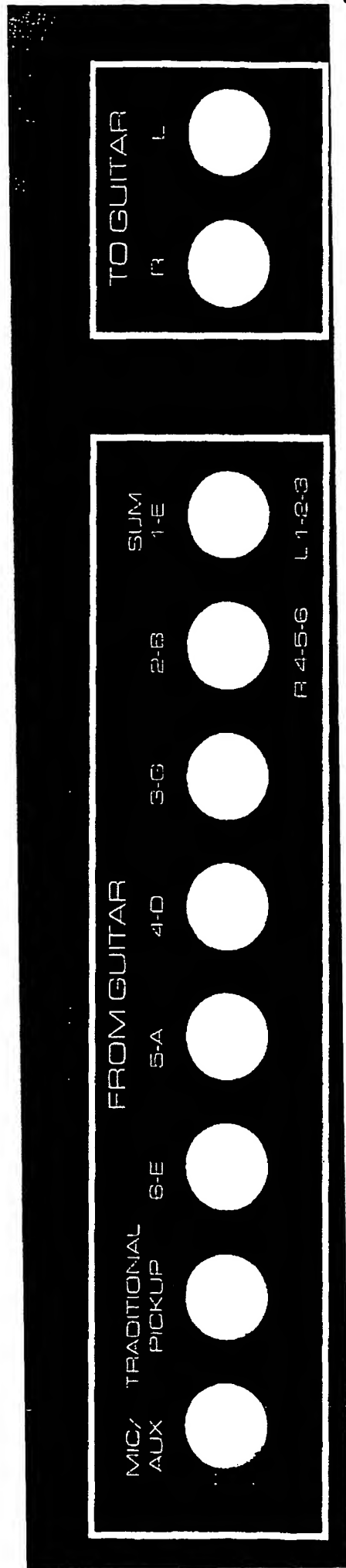


FIG. 22 B

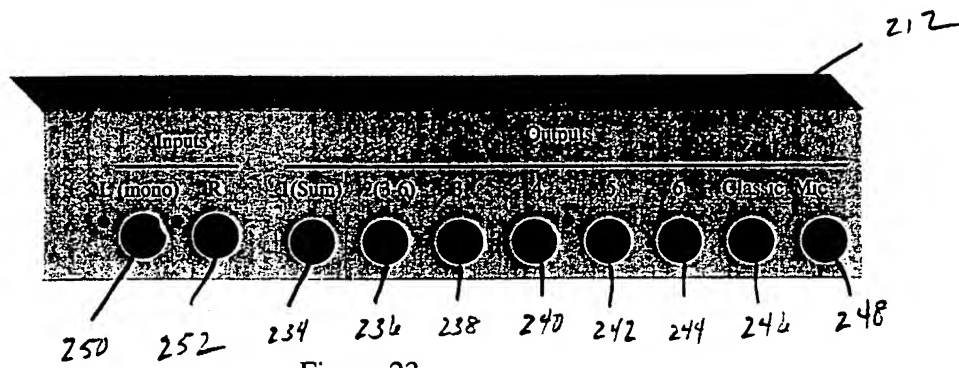


Figure 23

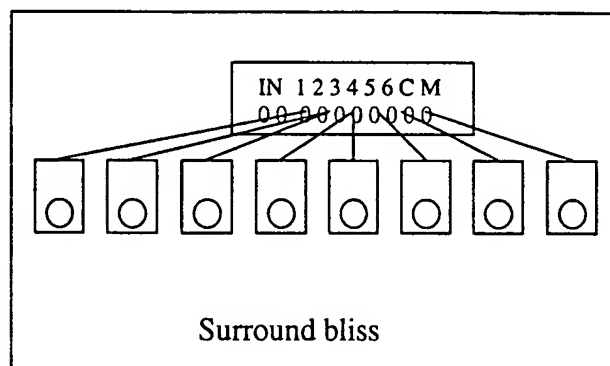
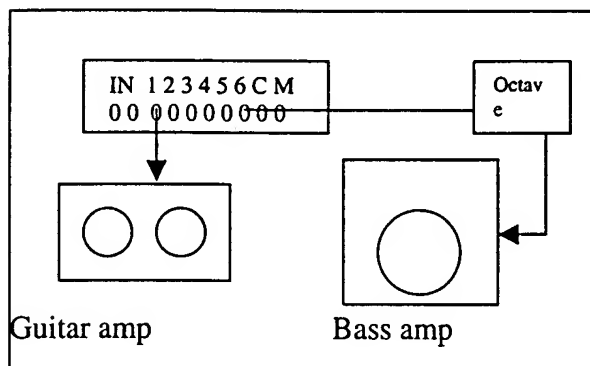
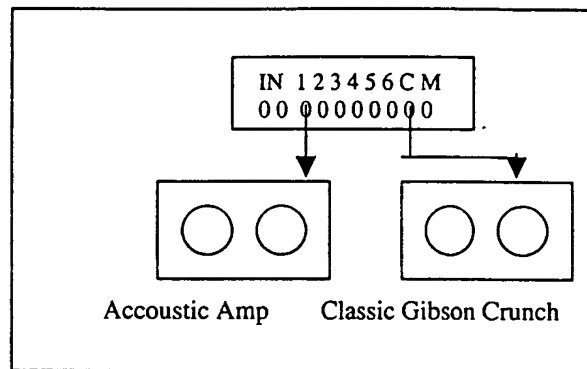
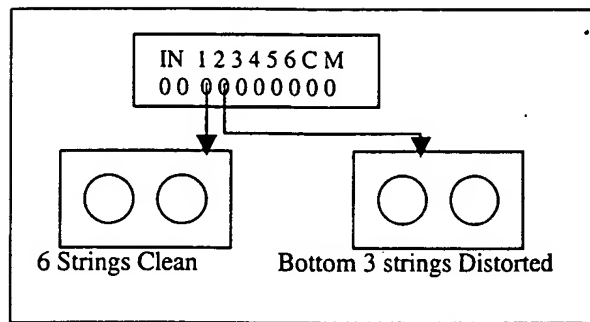


Figure 24

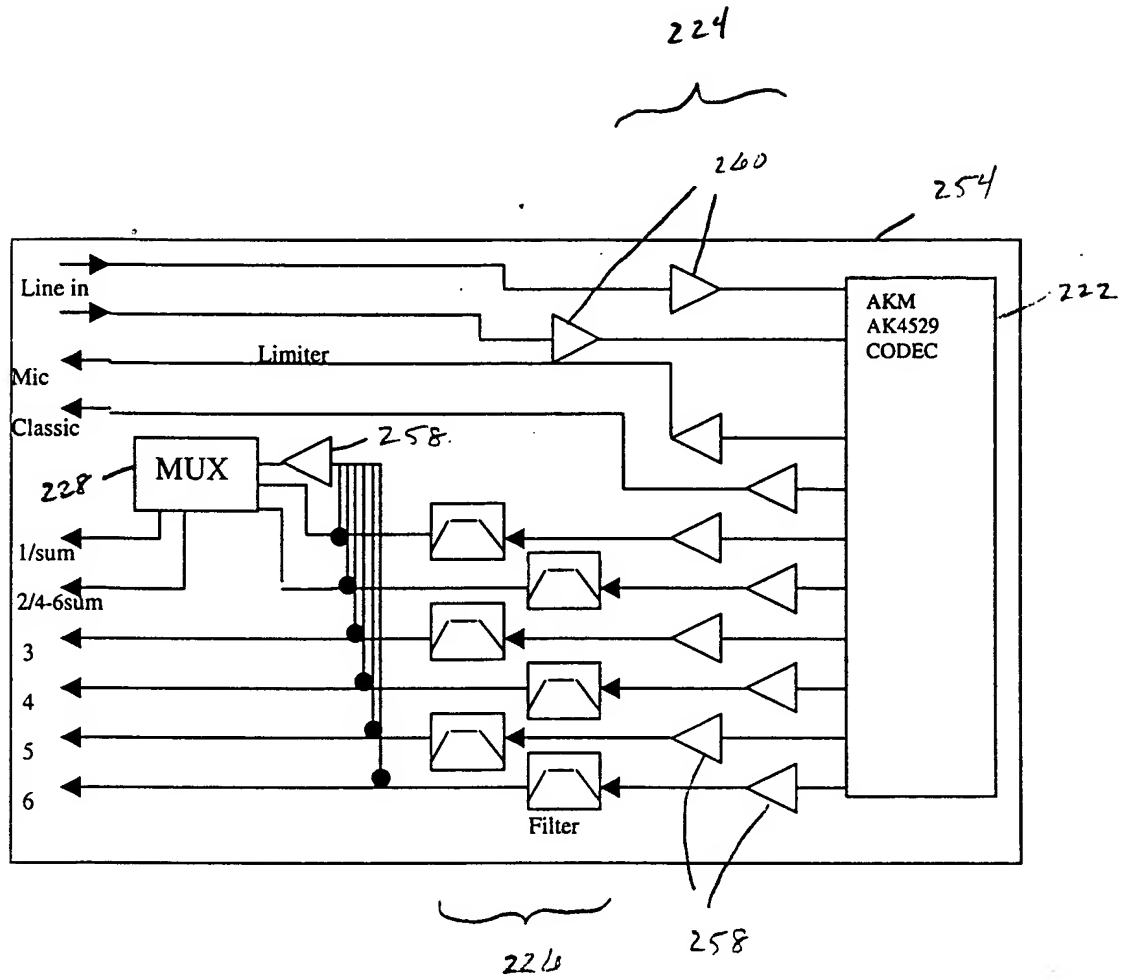


Figure 25

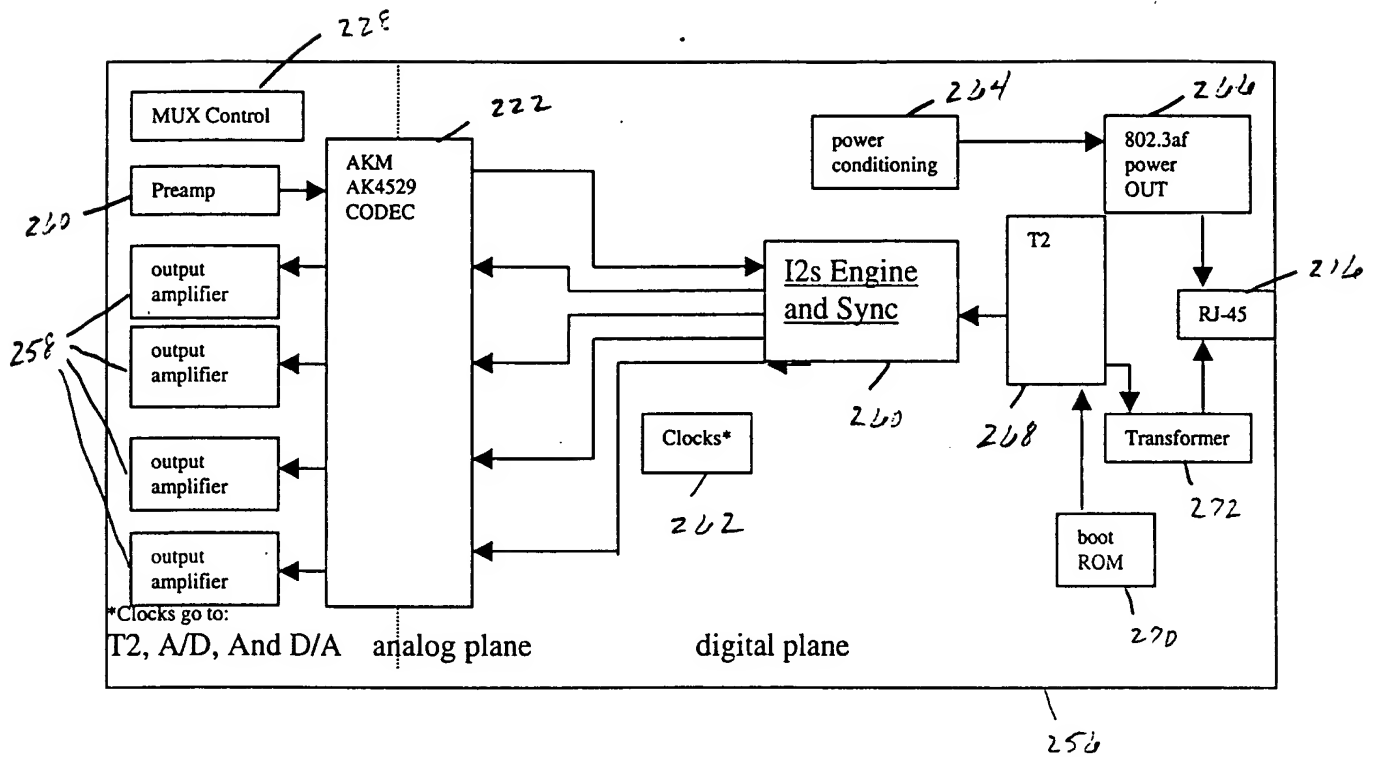


Figure 26

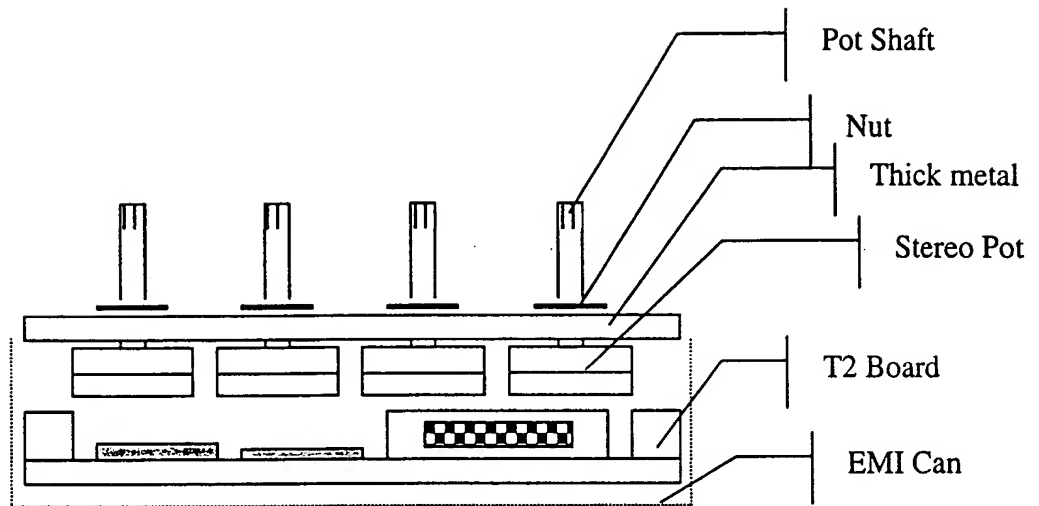


Figure 27

Passive Legacy system and Control Section

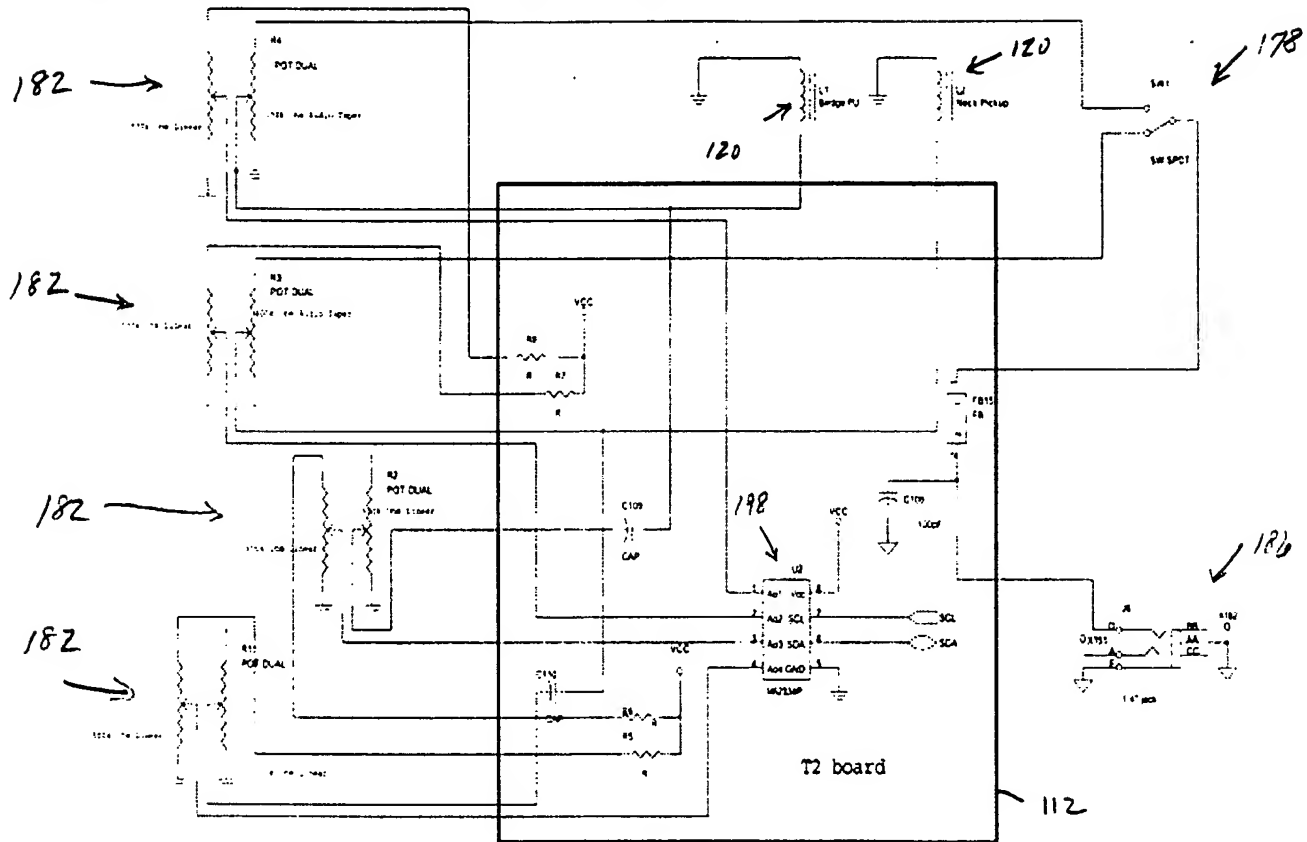
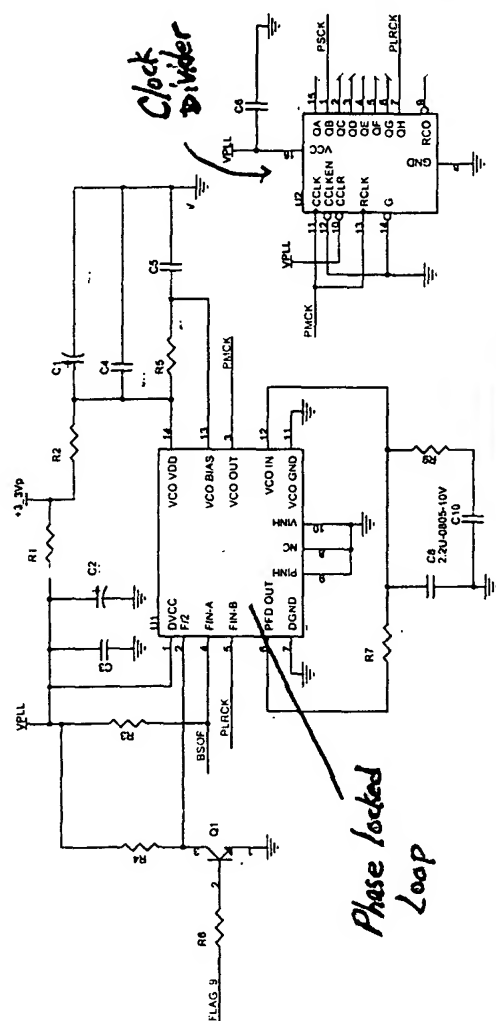
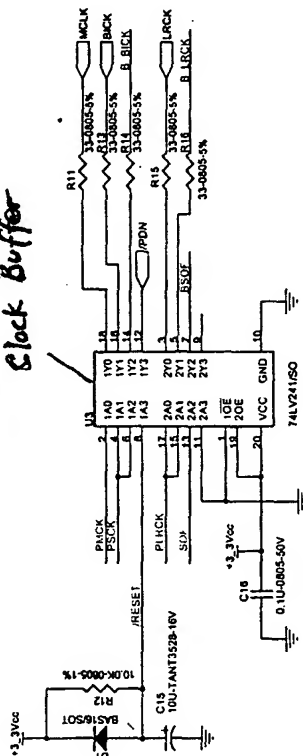


Figure 28

NOTE: All parts within this area powered by VPULL



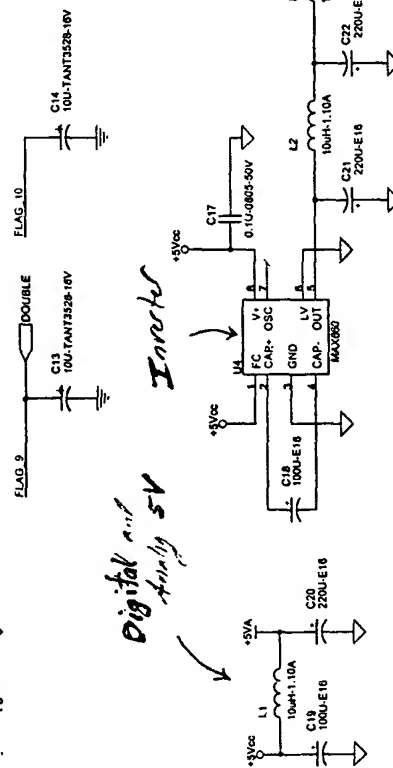
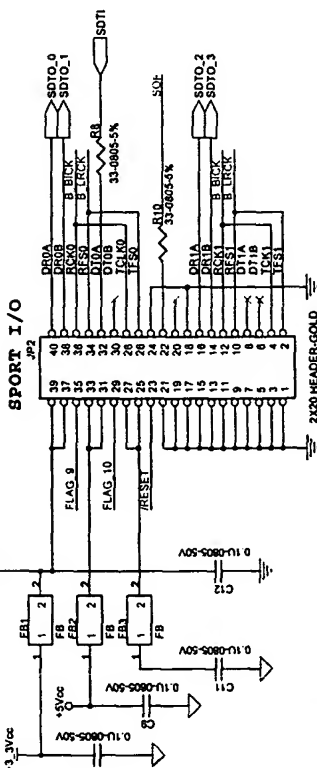
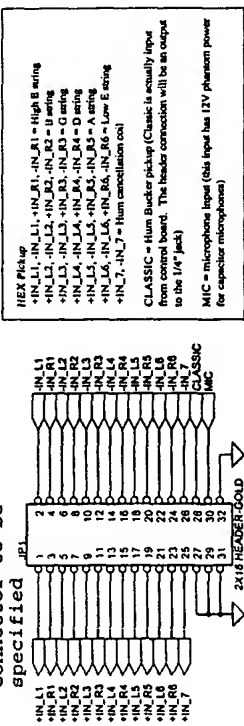
Clock Buffer



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Part	7283 Old Mountain View/Airco Rd. Unit F
Size	XY Pinamp
Doc	Document Number
Rev	8
File	80000
Date	1998-05-25 2002
Sheet	30

Fig 29

Pickup & Mic Inputs
Connector to be
specified



Digital and
Analog 5V
Inputs

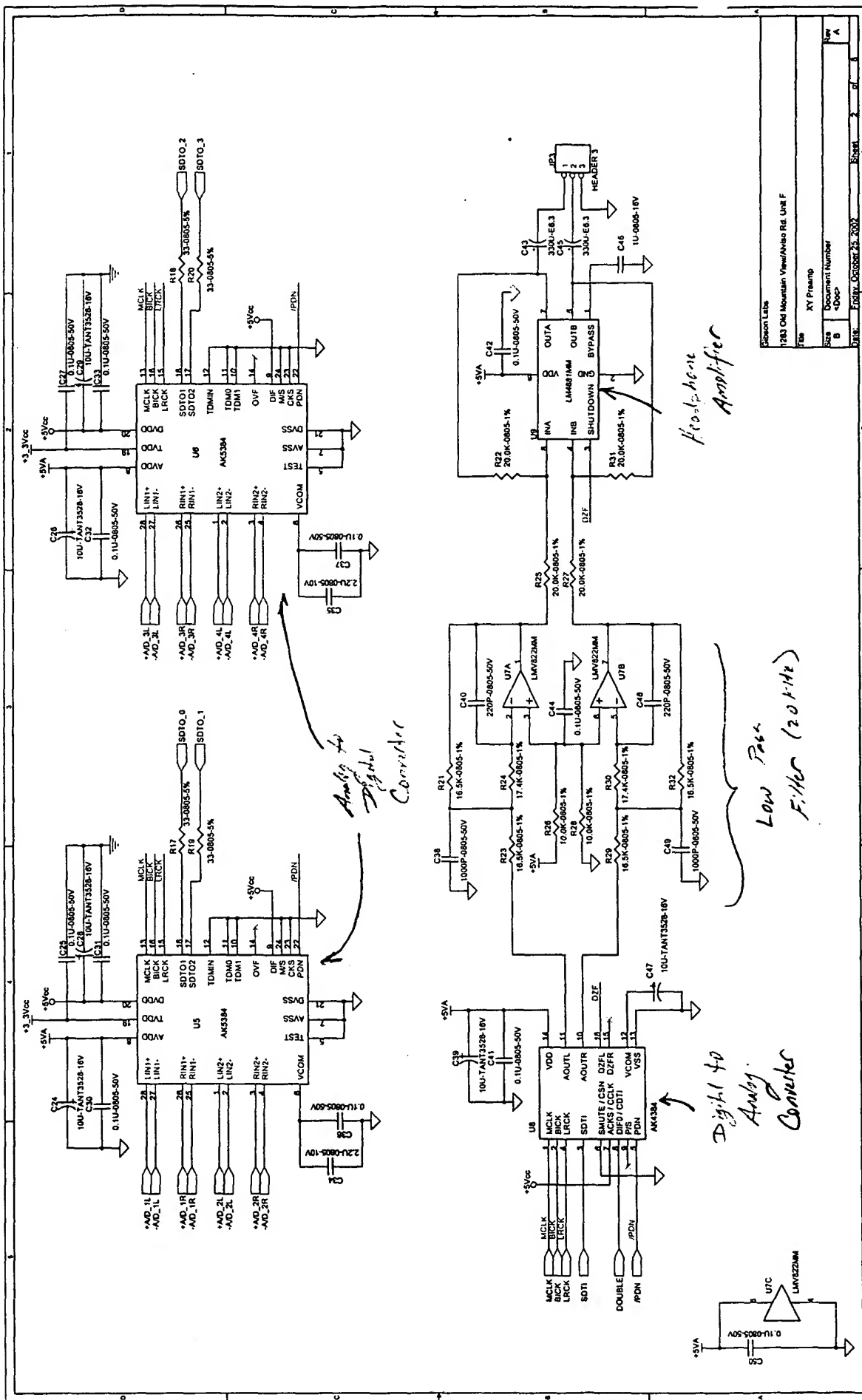


Fig. 30

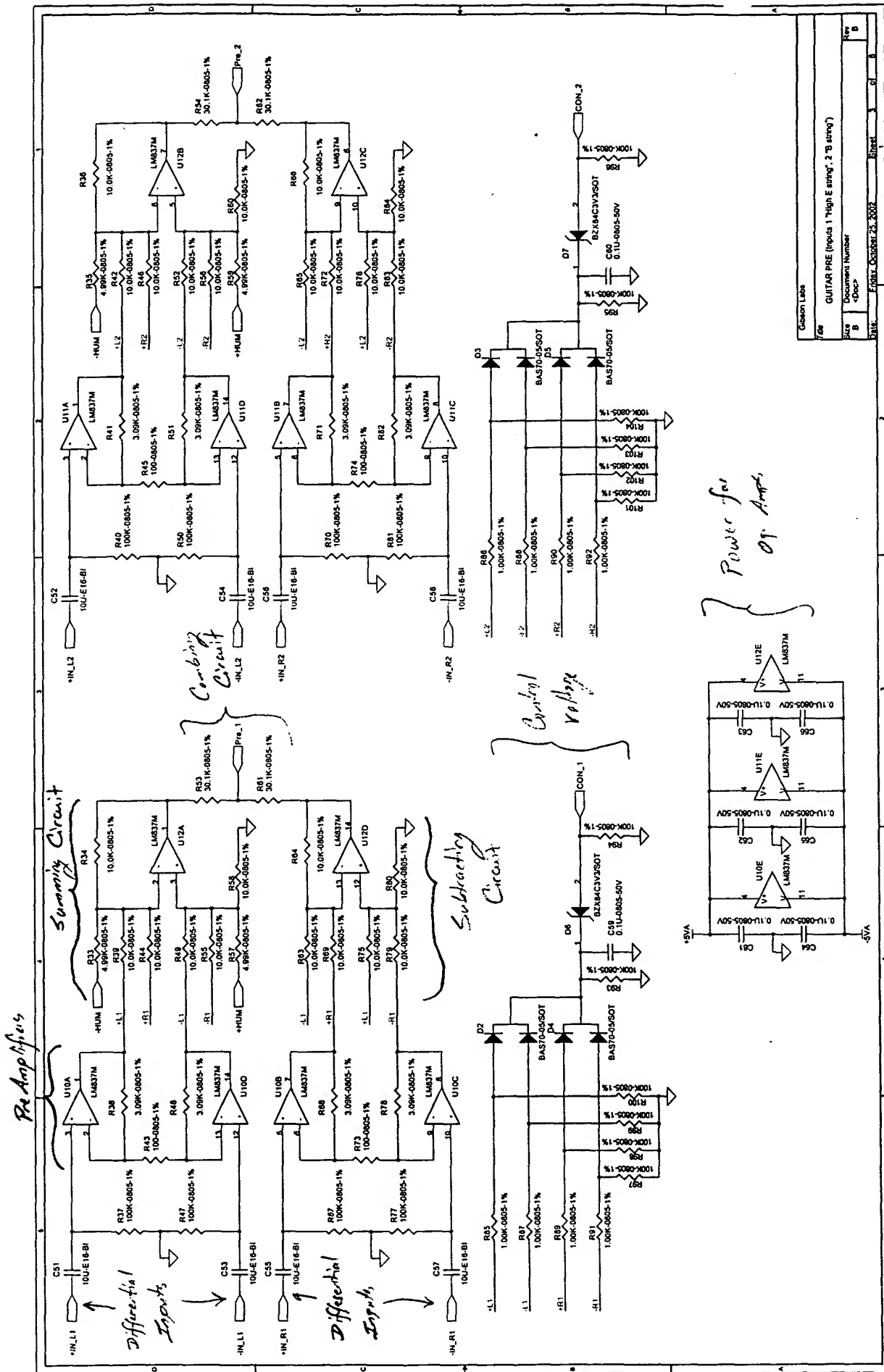
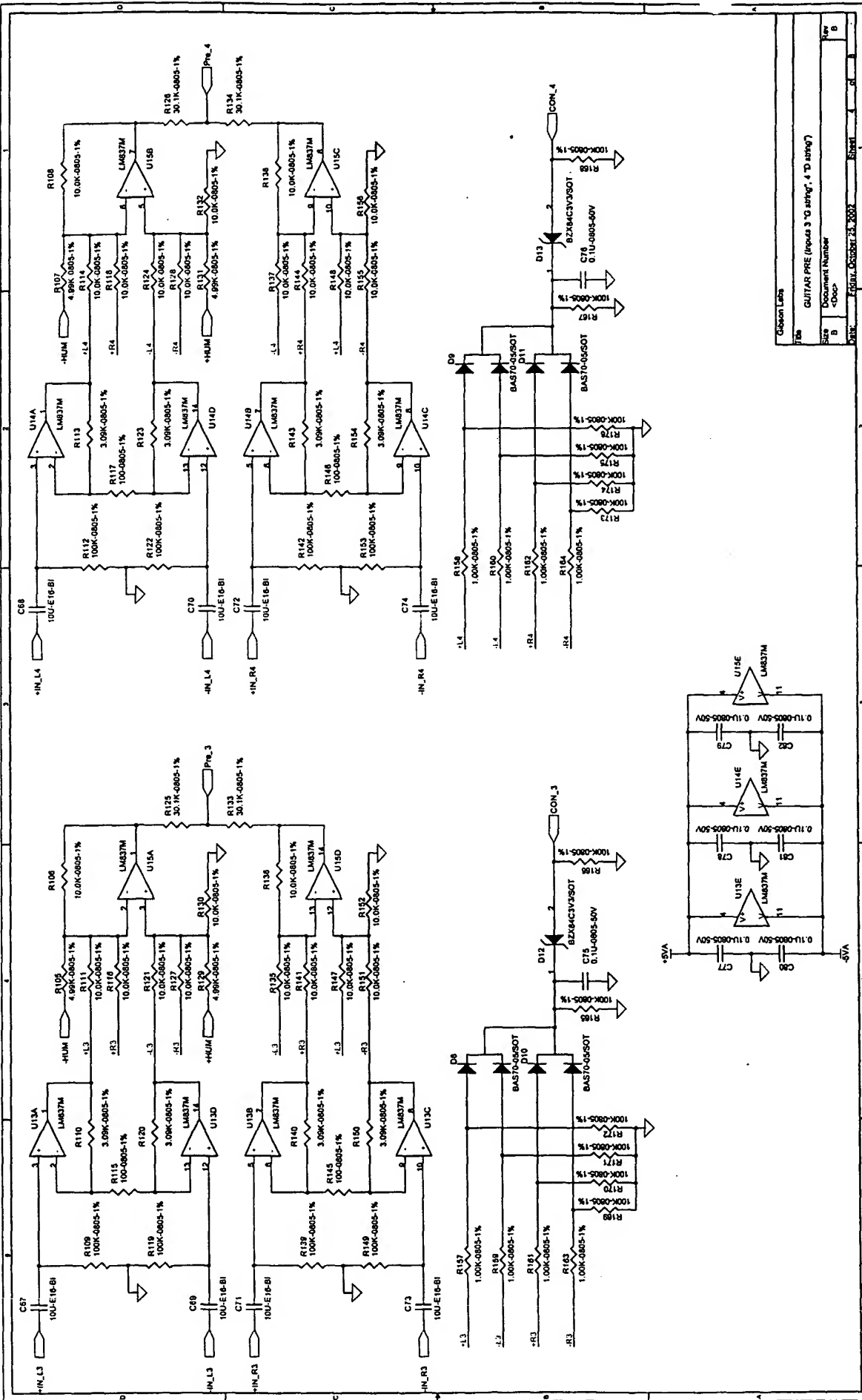
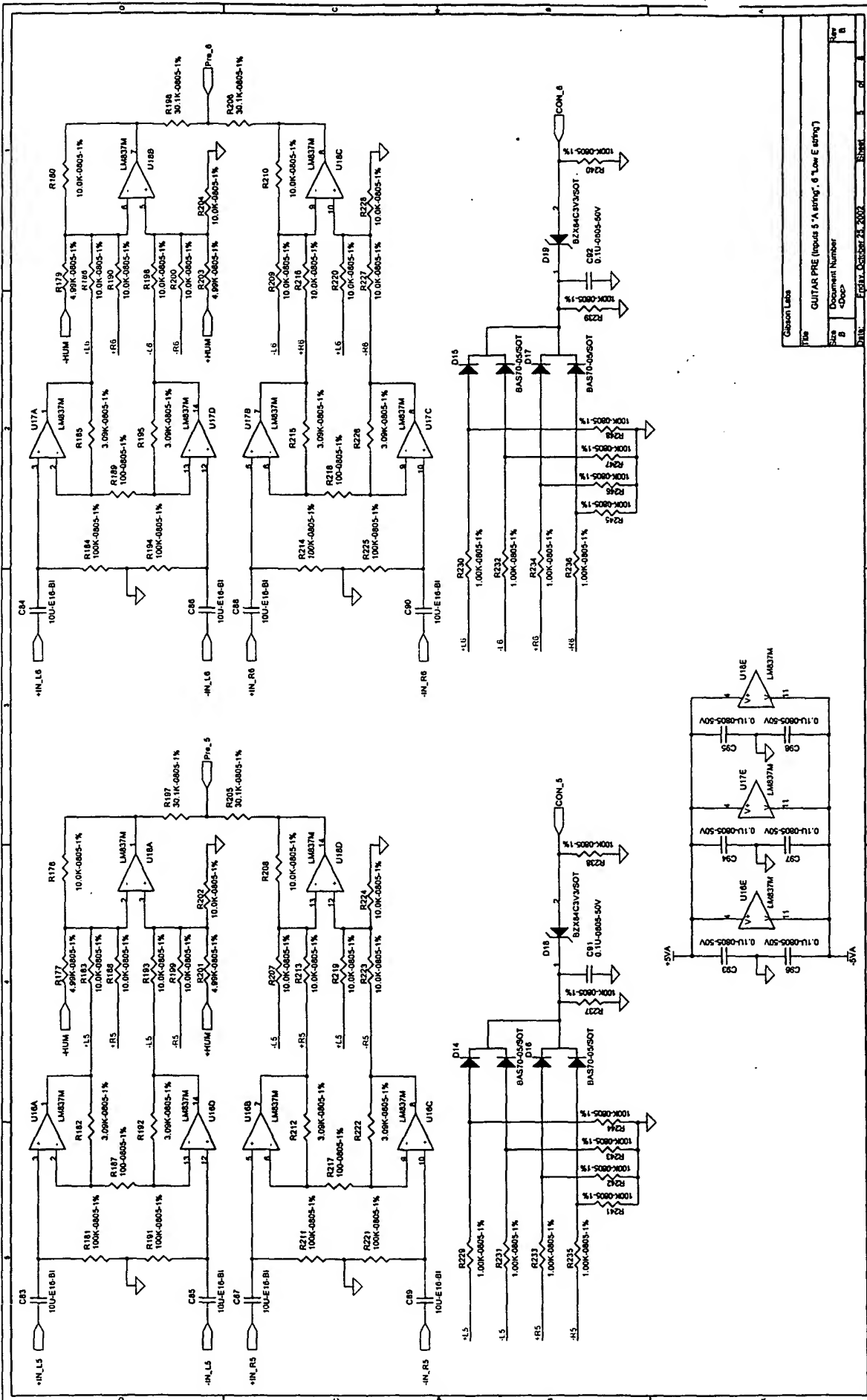


Fig. 31



Fg. 32



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Fig. 33

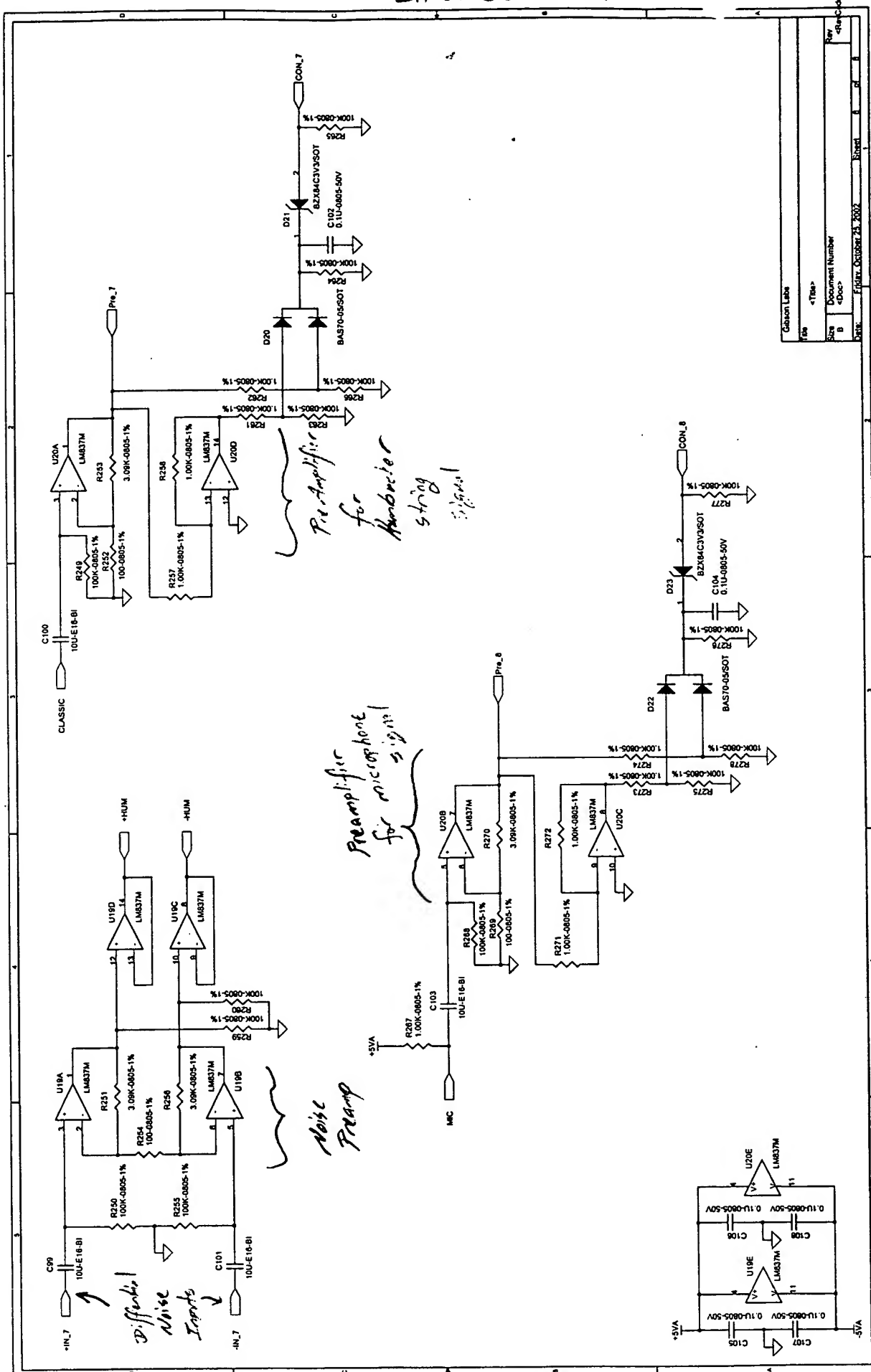


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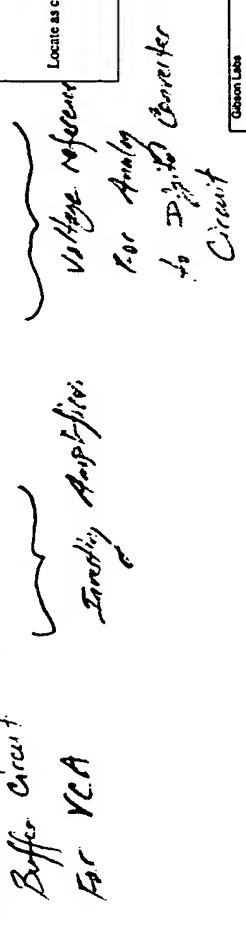


Fig. 35

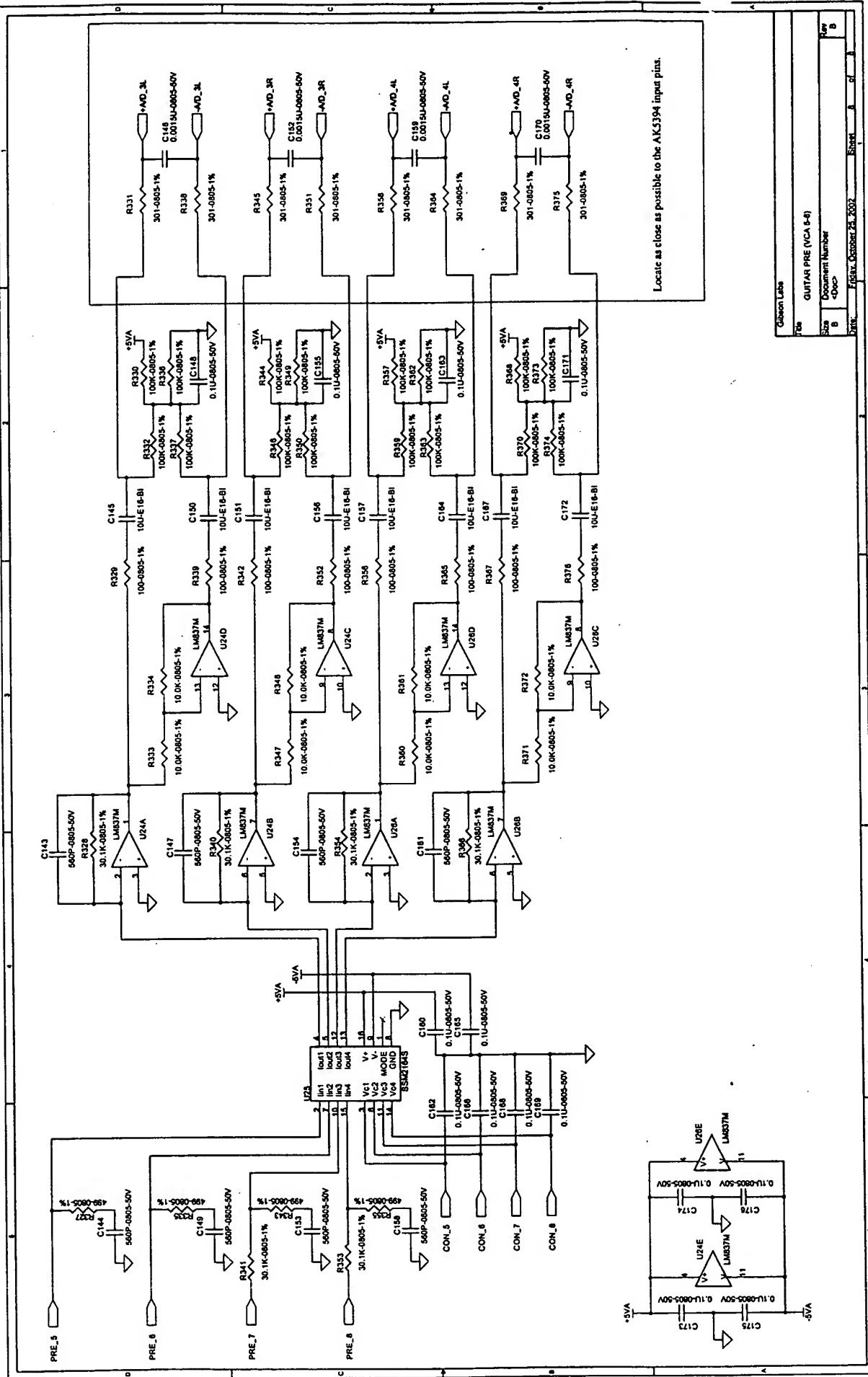


Fig 34